

Subject : I.T. Elective-III a) Design of Language Processors

Day : Monday
Date : 06/06/2016



Time : 10.00 A.M. TO 1.00 P.M.
Max Marks : 80 Total Pages : 1

N.B.

- 1) Attempt any **FIVE** questions from Section – I
 - 2) Attempt any **TWO** questions from Section – II.
 - 3) Figures to the right indicate **FULL** marks.
 - 4) Answer to both the sections should be written in the **SAME** answer book.
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SECTION – I

- Q.1** Discuss Non Deterministic Finite State Automata (Non-DFA) in detail. (10)
- Q.2** What is LL Grammar? (10)
- Q.3** Explain the concept of Static Linking in detail. (10)
- Q.4** Discuss the design of 2 Pass Assembler in detail. (10)
- Q.5** What is a Symbol Table? (10)
- Q.6** What is a Debugger? Discuss its functionality in detail. (10)
- Q.7** Write short notes on the following (**ATTEMPT ANY TWO**): (10)
- a) UNIX System V
 - b) Op-Code Table
 - c) Dynamic Storage Allocation

SECTION – II

- Q.8** Discuss the classification of grammars in detail. (15)
- Q.9** Explain following types of editors in brief: (15)
- a) Line Editor
 - b) Windows Editor
 - c) Screen Editor
- Q.10** What is Code Optimization and how it is applied to iterative loops? (15)

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